

2001 IEEE Workshop on  
**Neural Networks for Signal Processing**  
September 10–12, 2001 Falmouth, Massachusetts, USA

*Sponsored by the IEEE Signal Processing Society  
In cooperation with the IEEE Neural Networks Council*

## Call for Papers

### General Chairs

David J. MILLER  
Pennsylvania State University

Tülay ADALI  
University of Maryland  
Baltimore County

### Program Chairs

Jan LARSEN  
Technical University of Denmark

Marc VAN HULLE  
Katholieke Universiteit, Leuven

### Finance Chair

Lian YAN  
Athene Software, Inc.

### Proceedings Chair

Scott C. DOUGLAS  
Southern Methodist University

### Publicity Chair

Patrick DE MAZIERE  
Katholieke Universiteit, Leuven

### Registration

Elizabeth J. WILSON  
Raytheon Co.

### Europe Liaison

Hervé BOURLARD  
Swiss Federal Institute of  
Technology, Lausanne

### America Liaison

Amir ASSADI  
University of Wisconsin  
at Madison

### Asia Liaison

H.C. FU  
National Chiao Tung University

Thanks to the sponsorship of IEEE Signal Processing Society and IEEE Neural Network Council, the eleventh of a series of IEEE workshops on Neural Networks for Signal Processing will be held in Falmouth, Massachusetts, at the SeaCrest Oceanfront Resort and Conference Center. The workshop will feature keynote addresses, technical presentations and panel discussions. Papers are solicited for, but not limited to, the following areas:

**Algorithms and Architectures:** Artificial neural networks (ANN), adaptive signal processing, Bayesian modeling, MCMC, parameter estimation, nonlinear signal processing, generalization, design algorithms, optimization, Markov models, fuzzy systems (FS), evolutionary computation (EC), synergistic models of ANN/FS/EC, and wavelets.

**Applications:** Speech processing, image processing, blind source separation, sonar and radar, data fusion, data mining, intelligent multimedia and web processing, OCR, robotics, adaptive filtering, communications, sensors, system identification, and other general signal processing and pattern recognition applications.

**Implementations:** Parallel and distributed implementation, hardware design, and other general implementation technologies.

### Further Information

**NNSP'2001 webpage:** <http://eivind.imm.dtu.dk/nns2001>

### Paper Submission Procedure

Prospective authors are invited to submit a full paper of up to ten pages using the electronic submission procedure described at the workshop homepage: <http://eivind.imm.dtu.dk/nns2001>

Accepted papers will be published in a hard-bound volume by IEEE and distributed at the workshop. Extended versions of the best workshop papers will be selected and published in a Special Issue of an international journal published by Kluwer Academic Publishers.

### Program Committee

Yianni Attikiouzel	Andrew Back	Hervé Bourlard	Andrzej Cichocki
Jesus Cid-Sueiro	Robert Dony	Ling Guan	Tzzy-Ping Jung
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Kenneth Rose	Jonas Sjöberg	Robert Snapp	M. Kemal Sönmez
Naonori Ueda	Lizhong Wu	Lian Yan	Fernando J. Von Zuben

### Schedule

<b>Submission of full paper:</b>	<b>April 15, 2001</b>
<b>Notification of acceptance:</b>	<b>May 1, 2001</b>
<b>Submission of photo-ready accepted paper and author registration:</b>	<b>June 1, 2001</b>
<b>Advance registration, before:</b>	<b>July 15, 2001</b>